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(54) **Optical disk recording apparatus and recording control method**

(57) When the recording disk is in the partial recording condition in which the finalization is not completed, a control step (S63) to enter the inputted CD text data into the sub-code area of the program data and to record it in the program area, when the ejection command is received, is provided.

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EP 1 067 552 A2

## Description

[0001] The present invention relates to an optical disk recording apparatus and a recording control method, and specifically, to an optical disk recording apparatus and a recording control method, by which the related data relating to the optical disk such as the title or the name of a musical component, for example, the CD text is recorded in a read-in area of the optical disk.

[0002] Recently, the spread of an optical disk recording and reproducing apparatus (CD-R/RW recorder) in which a CD-R (Compact Disc - Recordable) or CD-RW (Compact Disc ReWritable) format is, adopted, is remarkable. According to this apparatus, when the program data is recorded in the program area and the disk is going to be ejected without finalizing (TOC data is written into the read-in area), initially, the contents data relating to the program data recorded in the program area, such as the program number, the starting time or completion time of each program, or the like, is temporarily recorded in a program memory area (PMA) provided inside the read-in area, and then, the disk is ejected.

[0003] However, in the PMA regulated by the CD-R or CD-RW format, even when it is judged from the viewpoint of its storage capacity, because the data format does not meet the CD text, it is difficult to record the CD text information.

[0004] Accordingly, in the CD-R/RW recorder, when the CD text is structured so as to be recordable, because the recorder can not temporarily store the CD text inputted by the user in the PMA, when the disk is ejected without being finalized, the inputted CD text information is cleared, and becomes useless at all. Therefore, it is necessary that, after the user completes the recording of all program in the CD-R, the CD text is inputted, and the disk is surely finalized. As described above, when the conventional CD-R/RW recorder is made to simply meet the CD text, there is a problem that the operability of the user for the CD text recording is very wrong.

[0005] On the one hand, the CD-R/RW recording software to control the CD-R drive for use in the personal computer is sold from many software manufacturers. In this recording software, there is the software satisfying the writing of the CD text, and in these software, there is the software having the function to temporarily store the CD text inputted by the user in the floppy disk drive (FDD) or hard disk drive (HDD) of the personal computer main body as a file so that the CD text inputted by the user does not become useless.

[0006] However, when the user continues to use this function, the files of the temporarily stored CD text are increased on and on in the disk drive, and there is a problem that it becomes difficult to found which file is the CD text file inputted and stored for which disk.

[0007] Also in the CD-R/RW recorder for the audio, the storage device such as the HDD or memory (SRAM)

is provided, and the CD text inputted by the user can be temporarily stored in them. However, these storage devices are very expensive, and specifically, in the memory, because the capacity of more than 8 kbytes per one sheet of the disk is necessary to store the CD text, when the storage of the CD text of about 100 sheets is considered, the considerably large capacity memory is required. Further, as described above, the problem that the correspondence relationship between the disk and the stored file is not found, still remains.

[0008] The present invention provides an optical disk recording apparatus by which the above problems are solved, and is provided with a related data recording control means for temporarily recording the related data which is recorded in a read-in area into a program area of an optical disk. The related data recording control means temporarily records the related data recorded in the read-in area into the program area of the optical disk, when the optical disk is in a partial recording condition in which the finalization is not completed. Accordingly, even when the optical disk is ejected without being finalized, the inputted related data does not become useless. Further, because the recording is conducted on the disk, another storage device is not necessary, and further, the correspondence relationship of the disk with the related data can be always maintained.

[0009] Further, the present invention provides a recording control method by which the above problems are solved, and is provided with a step to temporarily record the related data which is recorded in a read-in area into a program area. When the optical disk is in a partial recording condition in which the finalization is not completed, this step is conducted, and the related data is temporarily recorded in the program area of the optical disk. Accordingly, even when the optical disk is ejected without being finalized, the inputted related data does not become useless. Further, because the recording is conducted on the disk, another storage device is not necessary, and the correspondence relationship of the disk with the related data can be always maintained.

## In the Drawings

### [0010]

Fig. 1 is a signal arrangement conceptual view of the CD-R and CD-DA.

Fig. 2 is a view showing the data format of one frame after EFM modulation.

Fig. 3 is a view showing the sub-code frame in Fig. 2.

Fig. 4 is a view showing the detail of the sub-code portion in Fig. 3.

Fig. 5 is a view showing the sub-code portion in the read-in area.

Fig. 6 is a view showing the format of the sub-code Q block in Fig. 5.

Fig. 7 is a view showing the detail of the R - W block

in Fig. 5.

Fig. 8 is a view showing the format of each pack in Fig. 7.

Fig. 9 is a view showing the encoding format of the PMA.

Fig. 10 is a block circuit diagram showing a circuit of an embodiment of a disk recording and reproducing apparatus to which the present invention is applied. Fig. 11 is a flow chart used for the description of the operation of the apparatus of the present embodiment.

Fig. 12 is a flow chart used for the description of the operation of the apparatus of the present embodiment.

Fig. 13 is a flow chart used for the description of the operation of the apparatus of the present embodiment.

Fig. 14 is a flow chart used for the description of the operation of the apparatus of the present embodiment.

Fig. 15 is a flow chart used for the description of the operation of the apparatus of the present embodiment.

Fig. 16 is a flow chart used for the description of the operation of the apparatus of the present embodiment.

Fig. 17 is a flow chart used for the description of the operation of the apparatus of the present embodiment.

**[0011]** Referring to the attached drawings, an embodiment of the present invention will be described below. Fig. 1 shows signal arrangement conceptual views of a CD-R (Compact Disc - Recordable) and CD-DA (Compact Disc - Digital Audio) which are recorded and reproduced by the apparatus of the present embodiment, and (A) - (C) respectively show a signal arrangement conceptual view of a blank CD-R, CD-R which is in the partial recording condition in which the finalization is not completed, and the CD-R in which the finalization is completed, and (D) shows a signal arrangement conceptual view of the CD-DA.

**[0012]** Initially, the signal arrangement of the CD-DA shown in Fig. 1(D) and its signal format will be described. As shown in Fig. 1 (D), a CD-DA 10 is formed of, from its inner periphery, a read-in area 11 which is a control area in which the control data (TOC data) controlling a program is recorded, a program area 12 in which the program data is recorded, and a read-out area 13 in which the read-out data showing the completion of the program data is recorded.

**[0013]** The program data includes the audio data (voice data), which is the main data, and the sub-code data. The audio data is sampled at 16 bits, 44.1 kHz, and 1 word 16 bits is divided into the higher 8 bits and the lower 8 bits, and is defined as a symbol unit. An error correction coding processing and interleave processing are added to the symbol unit.

**[0014]** Each symbol is converted from 8 bits to 14 bits by the EFM modulation. Fig. 2 shows the data format of 1 frame (24 symbols = 588 bits) after the EFM modulation. A frame 100 is composed of a synchronous pattern area 101 of 24 bits, sub-code area 102 of 14 bits, main data area 103 including the main data D1 - D12 of 12 symbols, parity data area 104 including the parity data Q1 - Q4 of 4 symbols, program data area 103' including the main data D13 - D24, and parity data area 104' including the parity data P1 - P4. Three connection bits are placed between each area and each data, and 1 frame 100 is formed of total 588 channel bits.

**[0015]** One sub-code frame 105 is structured by 98 frames 100 (refer to Fig. 3). The sub-code frame 105 is composed of a frame synchronous pattern portion 106, sub-code portion 107, main data and parity portion 108.

**[0016]** Fig. 4 shows the detail of the sub-code portion 107. The first 2 frames F01 and F02 are the synchronous patterns S0 and S1 of the sub-code frame. Incidentally, as the synchronous pattern, a pattern of the out-of-rule which does not exist, is adopted by the EFM modulation. That is, S0 is "00100000000001" and S1 is "00000000010010". By the following frames F03 - F98, P channel to W channel of the sub-code are structured. In the P channel, the information showing the starting position of each program, and in the Q channel, the information such as the absolute time (A-Time) of the program, the elapsed time of the program (P-Time), and the number of program, and the like, is recorded. In the R channel to the W channel, the related data relating to the disk, specifically, the CD graphic (CD-G) information such as a still image or telop display, or the CD text information is recorded.

**[0017]** Fig. 5 shows the format of the sub-code portion 110 recorded in the read-in area 11. One sub-code frame is formed of 8 bits from the P channel to W channel, and the sub-code portion 110 is structured by 98 sub-code frames. The first 2 frames f01 and f02 are the synchronous patterns S0 and S1, and a sub-code P block 112 are structured by the P channel from the other frames f03 to f98, and a sub-code Q block 113 is structured by a Q channel from frame f03 to f98, and an R-W block 114 is structured by an R channel to W channel from the frame f03 to f98.

**[0018]** The format of the Q block 113 of the sub-code is shown in Fig. 6. The Q block 113 is composed of 96 bits, and 72 bits from TNO to PFRAME are data. The TNO is always 00, and a POINT shows the kind of the information from a MIN to the PFRAME. When the value of the POINT is 01 - 99, a PMIN, PSEC, PFRAME show the start address (absolute time) of each program. Further, when the value of the POINT is A0, the PMIN shows the first program number of the disk, and the other PSEC and PFRAME are 00. Further, when the value of the POINT is A1, the PMIN shows the last program number of the disk, and the other PSEC and PFRAME are 00. Further, when the value of the POINT

is A2, the PMIN, PSEC, and PFRAME shows the start address of the read-out area 13.

[0019] Fig. 7 shows the detail of the R-W block 114 shown in Fig. 5. The block 114 is structured by 96 symbols each of which is composed of 6 bits, and 24 symbols in which 96 symbols are divided by 4, are called 1 pack, and 4 packs are called 1 packet.

[0020] Each pack is formed of the format shown in Fig. 8. Four bytes from the top of the pack are allocated to an ID area 120, and a 12 bytes text area 121 in which the text information of 8 bit unit is recorded, follows after it, and the a CRC area 122 of 16 bits to detect the error is allocated to the last.

[0021] The ID area 120 is composed of ID1 - 1D4 separated by one byte unit, and in the ID1, the setting information to set the recording mode of the information recorded in the pack, and the discrimination information showing the kind of the text information are recorded. The higher 3 bits of the ID1 show the setting information, and in the case of the CD text, it is the mode 4 ("100"). The other lower bits of the ID1 show the discrimination information. The ID2 shows to which track the text information belongs. The ID3 is the consecutive numbers attached to the blocks. The ID4 shows the character code and the character position of the character rows of the present pack.

[0022] Next, the signal arrangement of the CD-R and its signal format will be described. The blank CD-R has a pre-groove (not shown) which is weaving (wobbling) in the radial direction with the center frequency of 22.05 kHz over almost the whole of its recording surface as shown in Fig. 1 (A). This pre-groove is used not only for guiding the light beam from the optical head, but also for showing the address information and for controlling the disk rotation.

[0023] Fig. 1(B) shows the signal arrangement of the CD-R 20 which is in the partial recording condition in which the finalization is not completed. According to the drawing, in the CD-R 20, programs P1 - P3 are recorded in a program area 22, a trace on which a certain trial writing is conducted to obtain the optimum power for each disk, exists in a power calibration area (PCA) 24 prepared inside the read-in area 21, and the contents data upto the program P3 which is recorded already, is recorded in the program memory area (PMA) 25, in order to enable the program to be additionally written in the program area 22. Incidentally, this contents data is composed of the following 4 data: (1) the track number including the start time and the end time; (2) the disk identification to discriminate each disk; (3) the skip/un-skip information; and (4) the RID code in the user data field of the TOC.

[0024] Herein, referring to Fig. 9, the encoding format of the PMA 25 will be described. The S0 and S1 are the same synchronous patterns as the S0 and S1 (Fig. 5) of the CD-DA, and the copy protection information and emphasis information are recorded in a CONTROL. A TNO is always 00. A ZERO is a counter showing con-

tinuous frames, and a CRC is an error correcting code.

[0025] An ADR shows the kind of the information from the POINT to PFRAME except for the ZERO. When the ADR is 1, the POINT shows the track number, the MIN, SEC and FRAME show the end time of the track, and the PMIN, PSEC and PFRAME show the start time of the track. When the ADR is 2, it is shown that the POINT to PFRAME are the data relating to the disk discrimination.

[0026] When the ADR is 3, the POINT shows the total number of tracks which are track skip-designated, and can obtain the maximum value of 21. That is, the total number of tracks which can be skip-designated is to the maximum 21 portions. In this case, the MIN - PFRAME respectively show the number of the track. For example, when the numbers of tracks which are track skip-designated, are 3, 4 and 7, the value of MIN is 3, the value of SEC is 4, and the value of FRAME is 7, and the values of remaining PMIN - PFRAME are 0. When the ADR is 4, reversely to the case of the ADR of 3, the POINT shows the un-skip information. Incidentally, when the tracks which are skip-designated or un-skip-designated, are not smaller than 7 portions, the numbers of tracks are recorded being separated into the plurality of block data as shown in Fig. 9.

[0027] On the one hand, when the ADR is 5, the POINT shows the total number of the ranges which are area skip-designated, and can obtain the maximum value of 40. That is, the portion which can be area skip-designated, is to maximum 40 portions. In this case, the MIN - FRAME show the end time of each range, and PMIN - PFRAME show the start time of each range. Accordingly, when 3 portions are area skip-designated, three of the blocks in Fig. 9 are recorded. Incidentally, the ADR = 6 - F, is prepared as the future standard.

[0028] Fig. 1(C) shows the signal arrangement of the CD-R 20 whose finalization is completed. The read-out data is recorded in the read-out area 23 following the end portion of the program area 22, and the control data (TOC data) including the contents data recorded in the PMA 25 is recorded in the read-in area 21. Accordingly, the CD-R 20 in which the TOC is recorded, can be reproduced by the common CD player in the same manner as the CD-DA 10.

[0029] Next, referring to Fig. 10, an embodiment of the optical disk recording and reproducing apparatus 50 of the present invention will be described. The disk 20 is transferred between the reproduction position at which the disk 20 can be loaded on the disk table 52, and the ejection position which is protruded outside the apparatus and at which the disk can be attached to and detached from the disk table 52, by the disk loading mechanism 51 structured by including the tray. The spindle motor 53 and the optical head 54 are moved up and down between a clamp position at which the disk 20 is clamped on the disk table 52, and an un-clamp position which is withdrawn downward, by the optical mechanism up and down mechanism 75. Incidentally, in the

drawing, the damper on the disk is omitted. The disk 20 loaded on the disk table 52 is driven by the spindle motor 53, and recording and reproducing are conducted by the optical head 54.

[0030] A digital input signal according to the digital audio interface format, inputted into the digital input terminal 55, is decode processed into the 44.1 kHz, 16 bits 2 channels digital audio data row, by the digital audio interface (DAIF) decoder 56, and inputted into a CD encoder 58 through a mute circuit 57. Incidentally, the digital audio data row is a 2'S complement code, and the mute circuit 57 conducts audio muting according to the mute signal (bar MUTE) from the system controller 66, which will be described later, by making all of the data row "L" status.

[0031] The CD encoder 58 converts the inputted data into the interleave whose 8 bits is defined as 1 symbol unit, error correcting code addition, sub-code addition, EFM modulation, synchronous frame addition, and NRZI (Non Return to Zero Inverse) signal, and outputs it to an LD power control circuit 59.

[0032] The sub-code is generated by the system controller 66. In the present embodiment, the system controller 66 is composed of 2 microcomputers (not shown). Each microcomputer is composed of a CPU, ROM, and RAM, and the first microcomputer (mechanism computer) controls mainly the mechanism control, and conducts the control of the ATIP decoder 65 or servo circuit 61, TOC or PMA control. The control conducted by the mechanism computer will be briefly described below.

#### (ATIP processing)

[0033] The wobble signal WBL is obtained from the reproduction head circuit and the following control is conducted.

- (1) The wobble signal is bi-phase demodulated.
- (2) The signal is separated into the sink, time data, and CRC.
- (3) The sink is made to the ATIP (Absolute Time In Pre-groove) sink, and converted into the timing signal.
- (4) The CRC of the time data is checked, and when there is no error, it is set as a determined value.

#### (Servo control)

[0034]

- (1) Open/close of the focus servo and tracking servo is switched.
- (2) The linear velocity constant control of the spindle servo is conducted.

#### (System protection processing)

[0035] In order to avoid the existence of the double recording or no-recording portion due to the interruption of recording or track off-positioning caused by the unexpected power failure or impact from the outside,

- (1) the recording position control, and
- (2) the observation of the tracking condition, are conducted, and when the abnormal condition is generated, the back-up circuit is operated, and protection processing is conducted and the quick release is conducted.

#### 15 (Setting of the optimum recording power)

[0036] Before the start of recording, the trial writing is conducted in the PCA area, and the optimum recording laser power is obtained, and is set in the APC circuit.

#### 20 (TOC (PMA) data generation/control)

[0037]

- (1) The assignment of the start position and end position of the additional recording
- (2) The recording of the end position of the additional recording
- (3) The generation, storing, and recording of the PMA
- (4) The generation and recording of the TOC.

#### (Sub-code data processing)

[0038] As the processing at the time of the sub-code recording,

- (1) the sub-code is generated, and
- (2) the sub-code addition timing is generated by using the ATIP sink, or the like.

[0039] As the processing at the reproduction,

- (1) the sub-code is extracted on the basis of the sink, and
- (2) the sub-code is decoded and sent to the display, or the similar devices.

[0040] On the one hand, the second microcomputer (mode computer) mainly conducts operation displays, and conducts the special function, system control, control of the digital input system, and switching of audio signal system.

[0041] The mode keys 70 operated by the user, such as the record key, reproduction key, stop key, ejection/loading key, finalizing key to designate the disk finalization, the text input key 71 from which the user inputs the CD text information, and the display 72 to dis-

play its operation condition or text information, are connected to the system controller 66, and the system controller 66 conducts the operation control of the overall apparatus according to the flow charts shown in Fig. 11 to Fig. 17, which will be described later.

[0042] Further, to the system controller 66, the CD text memory 69 in which the text data inputted by the operation of the text input key 71 is stored, is connected. The system controller 66 supplies the CD text data stored in this CD text memory 69 to the CD text encoder 74. Further, the system controller 66 generates the sub-code P and Q channels, and supplies them to the sub-code processor 73.

[0043] The CD text encoder 74 encodes the sub-code of the R - W channels, and supplies it to the sub-code processor 73. The sub-code processor 73 adds the error correcting code to the inputted sub-code of the Q channel, and supplies the sub-codes of the P - W channels to the CD encoder 58.

[0044] The LD power control circuit 59 supplies the signal supplied from the CD encoder 58, to the optical head 54, and has the adjustable laser power circuit by which the laser power of the optical head 54 is adjustably set. This setting is conducted in such a manner that, by conducting the trial writing in the PCA 24 (refer to Fig. 1), the optimum recording laser power value obtained by the system controller 66 is set.

[0045] On the one hand, the reproduction signal read out by the optical head 54 is inputted into the reproduction head circuit 60. The reproduction head circuit 60 conducts the processing of the generation of the RF signal and its binarization, and generation of the tracking error signal and the focus error signal. Each error signal is inputted into the servo circuit 61, and the tracking servo control and the focus servo control of the optical head 54 are conducted.

[0046] The RF signal which is binarized by the reproduction head circuit 60 is inputted into the CD decoder 62. The CD decoder 62 conducts the same processing as the common CD player, and the inputted signal is EFM demodulated, error corrected, and de-interleaved, and converted into the 44.1 kHz, 16 bits, 2 channel digital audio data row, and outputted. This data row is inputted into the digital audio interface encoder 63, and outputted from the digital output terminal 64 as the digital output signal according to the digital audio interface format.

[0047] Further, the wobble signal generated in the reproduction head circuit is binarized, and also inputted into ATIP (absolute Time In Pre-groove) decoder 65. The ATIP decoder 65 FM-demodulates the wobble signal, extracts the ATIP sink signal, and supplies it to the servo circuit 61. The servo circuit 61 conducts the spindle servo control according to the ATIP sink signal so that the linear velocity of the disk at the recording becomes constant. Further, the ATIP decoder 65 decodes processes the ATIP signal, and supplies the read-out address information to the system controller

66.

[0048] Further, the CD decoder 62 separates the sub-code at the EFM demodulation, and supplies the sub-code to the sub-code processor 67. The sub-code processor 67 detects the error of the sub-code, and separates the sub-code into the P channel, Q channel (refer to Fig. 6), and the R - W channels (refer to Fig. 7), and outputs them. The P channel and Q channel data are supplied to the system controller 66, and the R - W channel data is supplied to the CD text decoder 68.

[0049] The CD text decoder 68 decodes the sub-code of the R - W channels, and supplies to the system controller 66. The system controller 66 stores and holds the supplied sub-code in the CD text memory 69, and displays the CD text information on the display 72.

[0050] Next, referring to the flow chart shown in Fig. 11 to Fig. 17, an example of the operation of the apparatus of the above embodiment will be described. Initially, the operation when the ejection/loading key is operated, and the disk loading command is received, will be described.

[0051] When the disk loading command is received, the system controller 66 starts the control of whole system according to the flow chart shown in Fig. 11. Initially, the system controller 66, in step S1, controls the disk loading mechanism 51 and optical mechanism up-down mechanism 75, and sets the disk 20 at the ejection position onto the disk table 52. Next, the system controller 66, in step S2, judges whether loaded disk 20 is the CD-R (the finalized CD-R is not included) of the blank or in the partial recording condition, or not. This judgement is conducted by reproducing the read-in area of the disk 20 and by detecting the existence of the RF signal. When the disk is the CD-R (YES), the system controller 66 raises the write permission flag in step S3.

[0052] Following that, the system controller 66, in step S4, judges whether the CD-R is the blank disk. This judgement is conducted by reproducing the PMA 25, and by detecting the existence of the RF signal and by reading-in the sub-code information.

[0053] When it is the blank disk (step S3 = YES), the system controller 66, in step S5, controls the LD power control circuit 59 and conducts recording and reproducing with respect to the PCA area 24 of the disk, and finds the optimum writing power in step S6, and sets the writing laser power of the LD power control circuit 59.

[0054] On the one hand, when the CD-R is not the blank disk (step S4 = NO), the system controller 66, in step S7, reproduces the PMA data, and stores it in the internal RAM. Following that, the system controller 66, in step S8, judges whether the PMA data of the area skip designation (ADR = 5) exists. This is, as described later, for the reason why, when the apparatus of the present embodiment temporarily records the CD text program data in which the CD text data is inserted into the sub-code, in the program area 22, the recording area of the CD text program data is area-skip-design-

nated by the PMA data.

[0055] When the PMA data of the area skip designation exists (step S8 = YES), the system controller 66, in step S9, starts the reproduction of the range of the designated area skip (the range designated from the PMIN, PSEC, PFRAME to the MIN, SEC, FRAME in Fig. 9), and stores the CD text data recorded in the sub-code R channel - W channel included in the program data, in the CD text memory 69. When the PMA data having the area skip designation does not exist (step S8 = NO), the system controller 66 advances to the step S5.

[0056] On the one hand, when the CD-DA or the CD-R which is finalized, is loaded (step S2 = NO), the system controller 66, in the same manner as the ordinary CD player, in step S10, reproduces the TOC data, stores it in the internal RAM, and the control is completed. By the above description, the operation for the disk loading command is completed.

[0057] Next, the operation when the recording start command by the simultaneous operations of the REC key and PLAY key is received, will be described. Incidentally, it is assumed that the disk 20 is already set on the disk table, and the operation for the disk loading command is completed.

[0058] The system controller 66 starts the control of the overall apparatus according to the flow chart shown in Fig. 12, and initially, judges whether the write permission flag is raised, in step S20. When the writing, is permitted (step S20 = YES), the system controller 66 moves the optical head 54, in step S21, to the recording start position (when the program is already recorded, its end position), and starts, in step S22, the recording of the digital input signal inputted in the digital input terminal 55. On the one hand, when the writing is not permitted (step S20 = NO), the system controller 66 neglects this recording start command.

[0059] Next, the operation when the stop command by the operation of the stop key is received, will be described.

[0060] The system controller 66 starts the control of the overall apparatus according to the flow chart shown in Fig. 13, and in step S30, conducts the stop control of the recording condition or the reproducing condition in step S30, and its operation control is completed.

[0061] Next, the operation when the reproduction command by the operation of the reproduction key is received, will be described.

[0062] The system controller 66 starts the control of the overall apparatus according to the flow chart shown in Fig. 14, and in step S40, starts the reproduction of the disk. Incidentally, when the PMA data of the track skip designation or the area skip designation is stored, the system controller 66 skips the track or the area, and reproduces.

[0063] Next, in the stop condition, the operation when the text input is conducted by the operation of the text input key 71, will be described.

[0064] The system controller 66 fetches in the text data inputted by the operation of the text key 71, in step S50, according to the flow chart shown in Fig. 15, and in step S51, stores the text data in the CD text memory 69.

[0065] Next, the operation when the ejection command by the operation of the ejection/loading key is received, will be described.

[0066] The system controller 66 starts the control of the overall apparatus according to the flow chart shown in Fig. 16, and initially, in step S60, judges whether the text data is stored in the CD text memory 69. When the text data is not stored (step S60 = NO), the system controller 66, in step S67, generates the PMA data, and records the PMA data in the PMA 25 in step S65. After that, the system controller 66, in step S66, controls the disk loading mechanism 51 and the optical mechanism up and down mechanism 75, and moves the disk 20 to the ejection position.

[0067] On the one hand, when the text data which is key-inputted is stored in the CD text memory 69 (step S60 = YES), the system controller 66, in step S61, judges whether the finalization is completed. When it is finalized (step S61 = YES), the system controller 66 advances to the step S66, and moves the disk to the ejection position at once.

[0068] On the one hand, when it is not yet finalized (step S61 = NO), the system controller 66 controls the mute circuit 57 (Fig. 1) in step S62, and audio-mutes the digital audio data row outputted from the digital audio interface 56. Then, system controller 66, in step S63, records the text data stored in the CD text memory 69 in an area from the sub-code R channel to the W channel, in more detail, records the program data which is inserted into the R01 - W96 of the sub-code portion 107, succeeding to the last program in the program area 22.

[0069] Then, the system controller 66, in step S64, generates the PMA data which area-skip-designates (ADR = 5) the recording area of the CD text program data, and records the PMA data in the PMA 25 in step S65, then, moves the disk to the ejection position in step S66.

[0070] Finally, the operation when the finalization command by the operation of the finalization key is received, will be described.

[0071] The system controller 66 starts the control of the overall apparatus according to the flow chart shown in Fig. 17. The system controller 66, in step S70, judges whether the writing permission flag is set. When the writing is permitted (step S70 = YES), the system controller 66, in step S71, generates the read-out data, and records the read-out area 23 (Fig. 1 (C)) in the last of the program, and next, in step S72, when the CD text data exists in the CD text memory 69, generates the TOC data on the base of the CD text data and the PMA data, and when the CD text data is not recorded, generates the TOC data on the base of the PMA data, and records in the read in area 21. The disk 20 in which this

TOC data is written, has the same signal arrangement as the CD-DA 10 as shown in Fig. 1, and it can be reproduced by the common CD players. On the one hand, when the writing is not permitted (step S70 = NO), the system controller 66 neglects the finalization command.

[0072] Incidentally, the present invention is not limited to the above embodiments, and it is of course that it can be varied into various modifications without departing from the range described in Claims. For example, according to the apparatus of the embodiments, as shown in Fig. 16, corresponding to the ejection command, the program data (CD text program data) in which the CD text data is inserted into the sub-code, is recorded in the program area 22 (step S63 in Fig. 16), but, it may also be allowable that an exclusive use CD text temporary save key to temporarily save the CD text, is provided, and corresponding to the temporary save command by the operation of this key, this CD text program data is recorded in the program area 22.

[0073] Further, after the recording of the CD text program data, in step S64, the PMA data of ADR = 5 to area-skip-designate the recording area of the CD text program data is generated, and the PMA data is recorded in the PMA 25 in the step S65, however, it is not limited to the area-skip-designation, but, it is of course that the CD text program data may be skip-reproduced by the track skip designation (the PMA data of ADR = 3).

[0074] Further, in the CD player of the present condition, the products corresponding to the skip designation are still a few, and further, judging from the amount of the data of the CD text, because the reproduction time of the CD text program data is short, the skip designation is not always the necessary requirement.

[0075] Further, when the situation that the great part of the CD players meet the skip designation, comes in the future, because the CD text program data is surely skip-reproduced, it is not necessary to mute the output of the DAIF decode 56 at the time of program data recording.

[0076] On the one hand, according to the apparatus of the embodiments, the optical disk recording and reproducing, apparatus to conduct recording and reproducing on the CD-R is described, however, the kind of the disk is not limited to the CD-R, but, it is of course that the apparatus which conducts recording and reproducing on the disk according to the other format, such as the CD-RW having the same disk format as the CD-R, may also be allowable. Specifically, in the case of the CD-RW, because the re-writing and deleting can be conducted, when the data of the CD text included in the CD text program data is stored in the memory, the CD text program data can be deleted, or it can be deleted by being overwritten by the next music program data.

[0077] Further, according to the apparatus of the embodiments, an example in which the present invention is applied to the audio use optical disk recording and reproducing apparatus, is described, however, as

described in the conventional example, the present invention can also be applied to the recording software to control the CD-R drive connected to the personal computer.

[0078] Finally, it is described as an example that the CD text originally to be recorded in the control area is temporarily recorded in the program area, however, it is of course that the data is not limited to the CD text.

[0079] As described above, according to the present invention, because the related data recorded in the control area is temporarily recorded in the program area, the control data is not uselessly cleared. Further, because the disk is used as the temporary recording medium, the expensive HDD or memory is not necessary, and the cost is not increased. Further, because the related data is recorded on the disk, the correspondence relationship of the related data with the disk is always maintained, and the operability of the user is very excellent.

[FIG. 1]

[0080]

A:	PRE-GROOVE AREA
B:	BLANK CD-R
C:	PARTIALLY RECORDED CD-R
D:	PROGRAM DATA
E:	READ-IN AREA
F:	FINALIZED CD-R
G:	PROGRAM DATA
H:	READ-OUT DATA
I:	READ-OUT AREA
J:	PROGRAM AREA

[FIG. 2]

[0081]

A:	SYNCHRONOUS PATTERN AREA
B:	SUB-CODE AREA
C:	FRAME 100 (588 BITS)

[FIG. 3]

[0082]

A:	FRAME SYNCHRONOUS PATTERN PORTION
B:	SUB-CODE PORTION
C:	MAIN DATA AND PARITY PORTION
D:	SUB-CODE FRAME 105
E:	FRAME

[FIG. 4]

[0083]

A:	FRAME
----	-------



[FIG. 5]

[0084]

A: FRAME  
 B: SUB-CODE PORTION 110  
 C: SUB-CODE  
 D: PACK

[FIG. 6]

[0085]

A: SUB-CODE Q BLOCK 113 (96 BITS)

[FIG. 7]

[0086]

A: R - W BLOCK 114  
 B: TEXT AREA  
 C: 1 PACK (18 BYTES)  
 D: 1 PACKET (96 SYMBOLS)

[FIG. 8]

[0087]

A: 1 PACK (18 BYTES)  
 B: TEXT AREA  
 C: ID AREA 120 (4 BYTES)  
 D: CRC AREA 122 (2 BYTES)

[FIG. 10]

[0088]

50: OPTICAL DISK RECORDING AND REPRODUCING APPARATUS  
 51: LOADING MECHANISM  
 56: DAIF DECODER  
 57: MUTE CIRCUIT  
 58: CD ENCODER  
 59: LD POWER CONTROL CIRCUIT  
 60: REPRODUCTION HEAD CIRCUIT  
 61: TRACKING, FOCUS, SPINDLE SERVO CIRCUIT  
 62: CD DECODER  
 63: DAIF ENCODER  
 65: ATIF DECODER  
 66: SYSTEM CONTROLLER  
 67: PROCESSOR  
 68: CD TEXT DECODER  
 69: CD TEXT MEMORY  
 70: MODE KEY  
 71: TEXT INPUT KEY  
 72: DISPLAY  
 73: PROCESSOR

74: CD TEXT ENCODER

75: OPTICAL MECHANISM UP AND DOWN MECHANISM

5 [FIG. 11]

[0089]

S1: LOADING CONTROL  
 10 S2: CD-R ?  
 S3: SET WRITING PERMISSION FLAG  
 S4: BLANK DISK ?  
 S7: STORE PMA DATA  
 S8: AREA SKIP DESIGNATION ?  
 15 S9: REPRODUCE AREA SKIP RANGE AND STORE CD TEXT DATA  
 S5: RECORD AND REPRODUCE PCA AREA  
 S6: SET OPTIMUM POWER  
 S10: STORE TOC DATA

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[FIG. 12]

[0090]

25 S20: WRITING PERMITTED ?  
 S21: MOVE OPTICAL HEAD TO RECORDING START POSITION  
 S22: START RECORDING

30 [FIG. 13]

[0091]

S30: STOP CONTROL

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[FIG. 14]

[0092]

40 S40: REPRODUCTION CONTROL

[FIG. 15]

[0093]

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S50: FETCH TEXT DATA  
 S51: STORE DATA IN CD TEXT MEMORY

[FIG. 16]

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[0094]

S60: CD TEXT EXIST ?  
 S61: FINALIZATION IS COMPLETED ?  
 55 S62: MUTE CONTROL  
 S63: RECORD SILENT PROGRAM DATA INCLUDING CD TEXT  
 S64: DESIGNATE AREA SKIP AND GENERATE

PMA DATA

S65: RECORD PMA

S66: EJECTION CONTROL

S67: GENERATE PMA DATA

[FIG. 17]

[0095]

S70: WRITING PERMITTED ?

S71: RECORD READ-OUT

S72: GENERATE CONTROL DATA

S73: RECORD CONTROL DATA

#### Claims

1. A optical disk recording apparatus (50) which records a program area (22) in which program data is recorded, and a read-in area (21) in which the control data controlling the program data recorded in the program area (22) and the related data relating to a disk are recorded, into an optical disk (20),

said optical disk recording apparatus comprising:

when said optical disk (20) is in a partial recording condition in which the finalization is not completed, a related data recording control means (S63) for recording the related data which is recorded in the read-in area (21), in the program area (22).

2. The optical disk recording apparatus according to Claim 1, wherein

the related data is the text data which is set by a user, and is recorded in the program area (22) as a sub-code of the program data.

3. The optical disk recording apparatus according to Claim 2, further comprising:

a mute circuit (57) to audio-mute an audio data; and

a mute circuit control means (S62) for making said mute circuit (57) to be in the mute condition, while the program data in which the related data is inserted into the sub-code, is recorded in the program area (22), wherein the program data includes the audio data as the main data.

4. The optical disk recording apparatus according to Claim 1, further comprising:

a contents data generation means (S64) for generating the contents data, including a skip information to skip the reproduction of the pro-

gram data recorded in the program area (22); and

a contents data recording control means (S65) for recording the generated contents data in a program memory area (25), wherein when said optical disk (20) is in the partial recording condition, said optical disk (20) further has a contents area (25) to record the contents data of the program data which is recorded in the program area (22).

5. The optical disk recording apparatus according to Claim 4, further comprising:

a reproduction means (54, 60, 62) for reproducing said optical disk (20);

a memory (69) to store the related data; and a related data reproduction control means (S9) for reproducing the related data recorded in the program area (22), according to the skip information included in the contents data, and for storing the reproduced related data in said memory (69).

6. The optical disk recording apparatus according to Claim 5, further comprising:

a disk loading mechanism (51) to transfer said optical disk (20) between a reproduction position to reproduce said disk and an eject position at which said disk is ejected, wherein said related data reproduction control means (S9) corresponds to the fact that said optical disk (20) is transferred to the reproduction position by said disk loading means (51), and reproduces the related data recorded in the program area (22).

7. The optical disk recording apparatus according to Claim 5, further comprising:

a control data generation means (S72) for generating the control data including the related data stored in said memory (69), corresponding to a finalization command; and a control area recording control means (S73) for recording the generated control data in the control area (21).

8. The optical disk recording apparatus according to Claim 1, further comprising:

a disk loading mechanism (51) to disk-reproduce the optical disk (20); and an eject/load control means (S66) for controlling said disk loading mechanism (51), wherein said eject/load control means (S66) controls said disk loading mechanism and moves said

optical disk (20) to the ejection position, after said related data recording control means (S63) records the related data in the program area when an ejection command is inputted.

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9. The optical disk recording apparatus according to Claim 2, wherein

said related data recording control means (S63) records the related data in the program area, when a temporary save command to temporarily save the text data is inputted.

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10. A recording control method of an optical disk recording apparatus to record a program area (22) in which the program data is recorded, and a read-in area (21) in which the control data to control the program data recorded in the program area and the related data relating to said disk, are recorded, in an optical disk (20),

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said recording control method comprising:  
a step of recording said optical disk (20) in the partial recording condition in which the finalization is not completed,  
a step (S63) of recording the related data recorded in the read-in area (21) in the program area (22).

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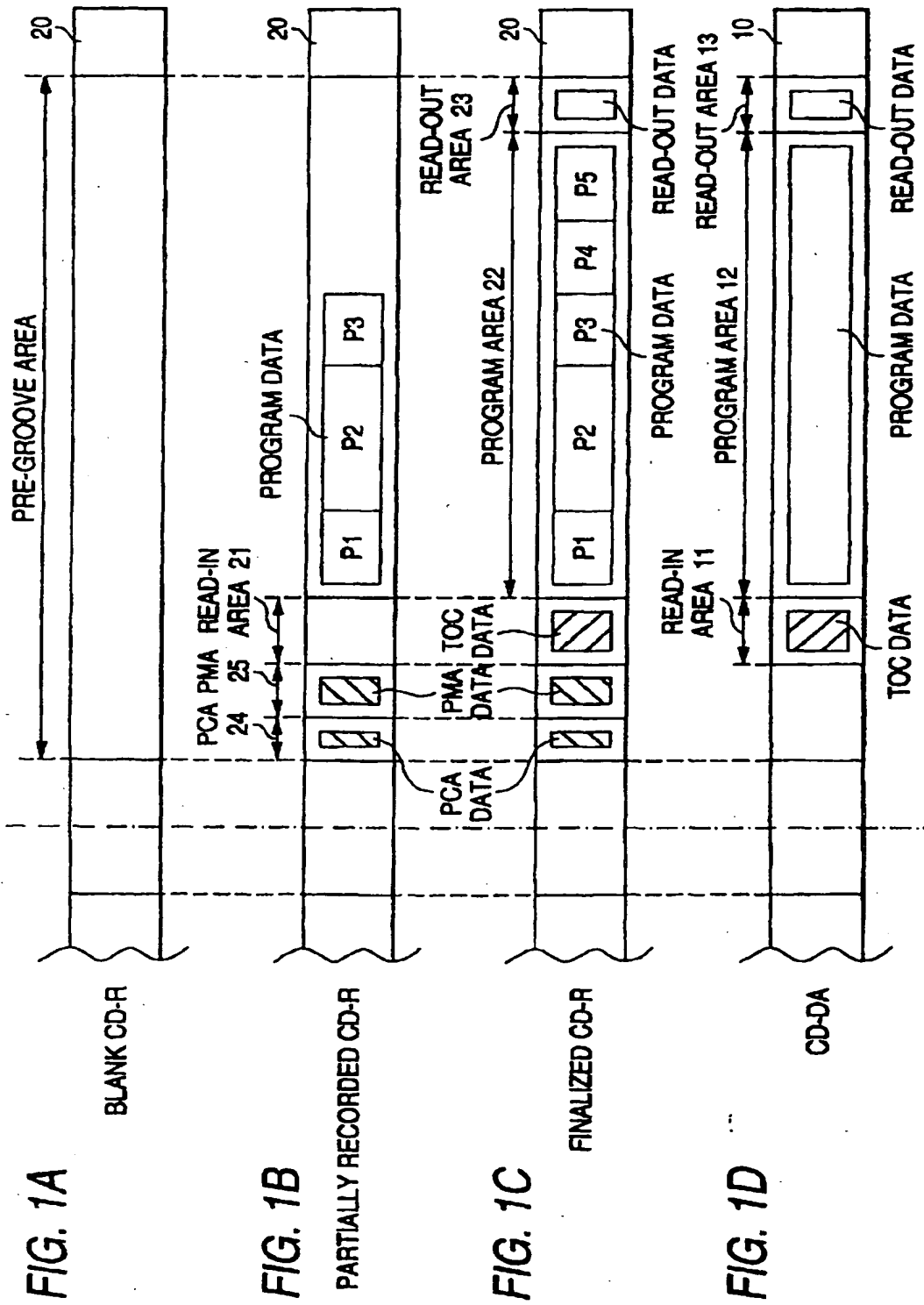
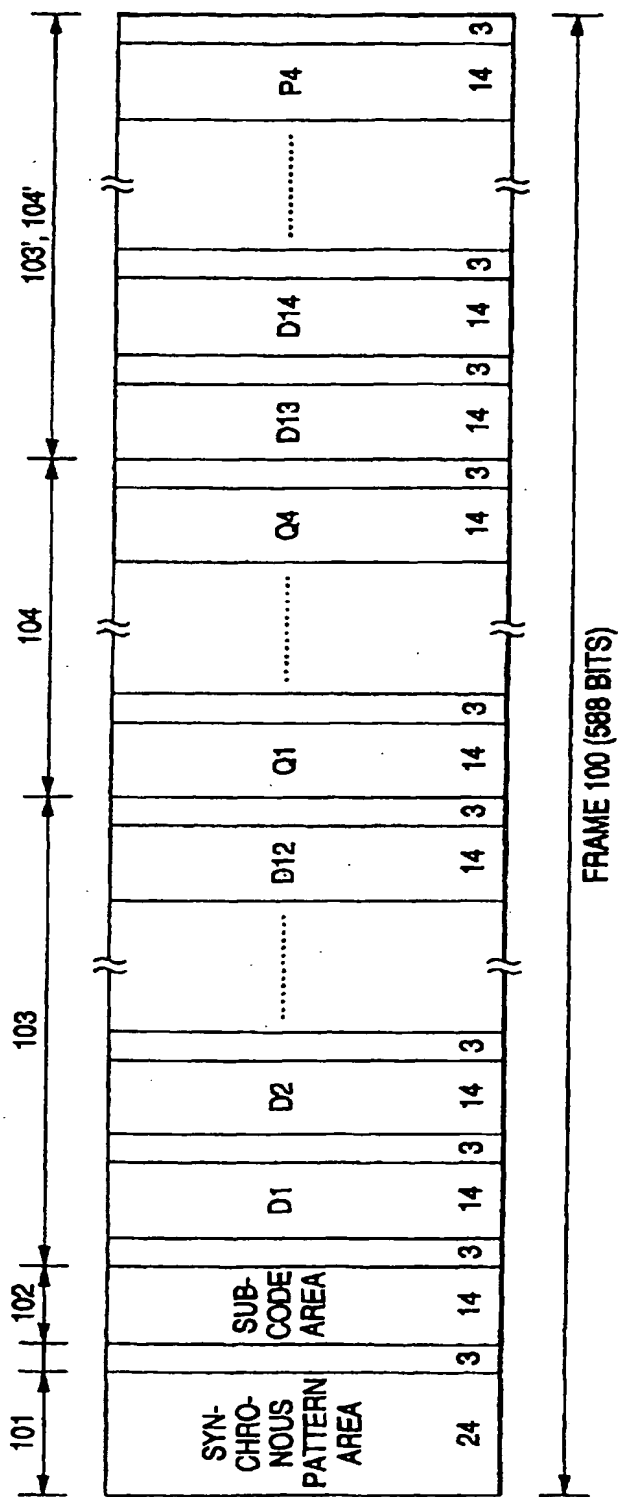
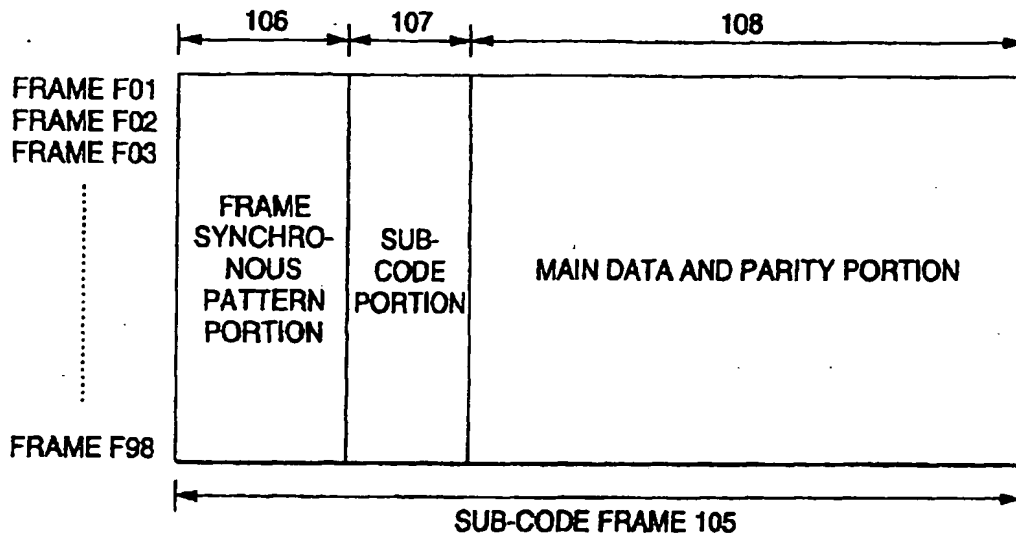


FIG. 2



**FIG. 3**



**FIG. 4**

	P	Q	R	S	T	U	V	W
FRAME F01	S0							
FRAME F02	S1							
FRAME F03	P01	Q01	R01	S01	T01	U01	V01	W01
FRAME F04	P02	Q02	R02	S02	T02	U02	V02	W02
FRAME F05	P03	Q03	R03	S03	T03	U03	V03	W03
⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
FRAME F97	P95	Q95	R95	S95	T95	U95	V95	W95
FRAME F98	P96	Q96	R96	S96	T96	U96	V96	W96

FIG. 5

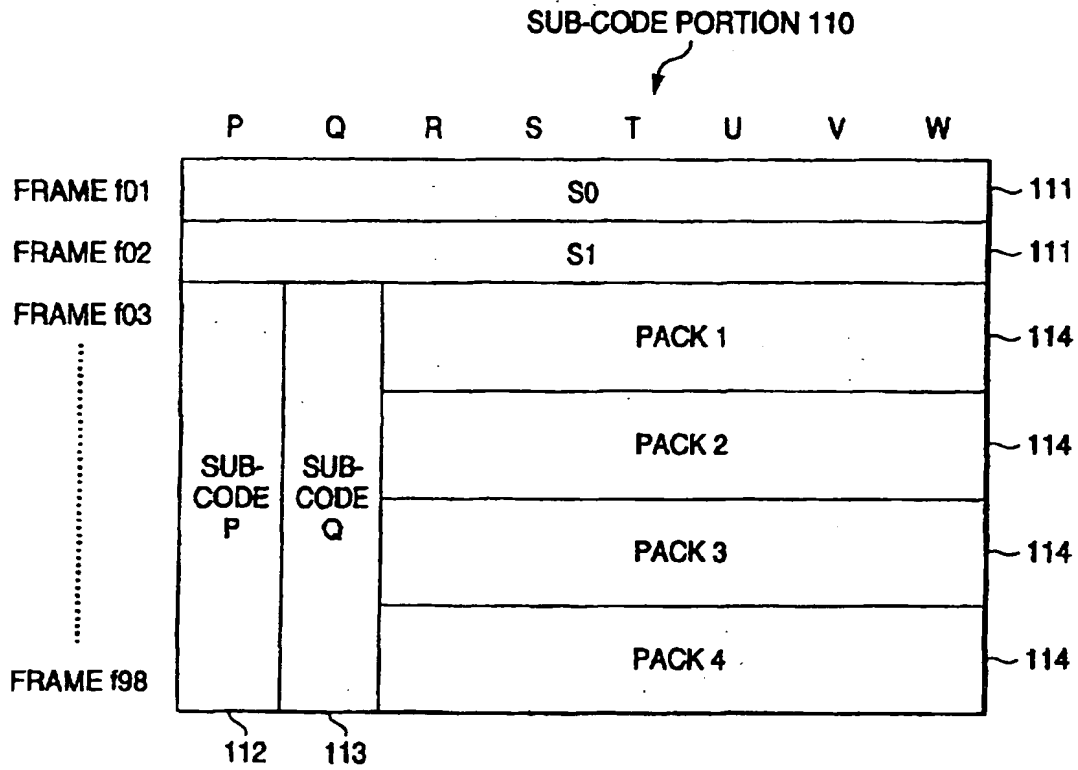


FIG. 6

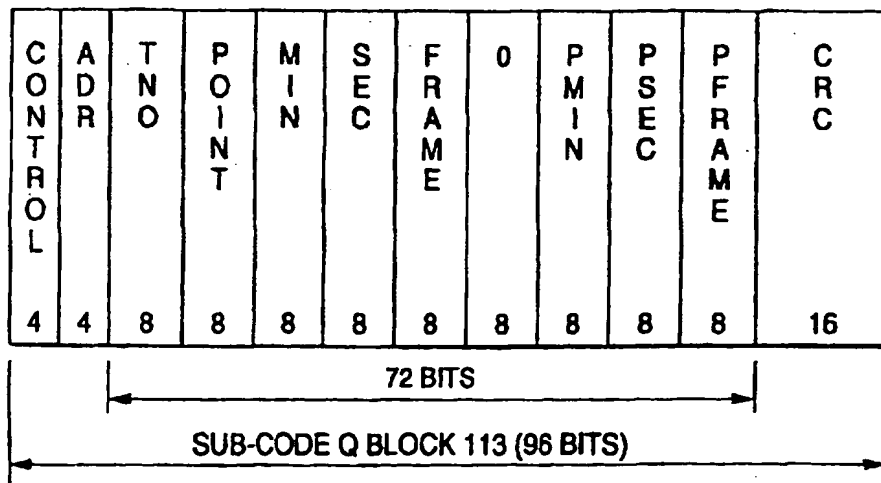
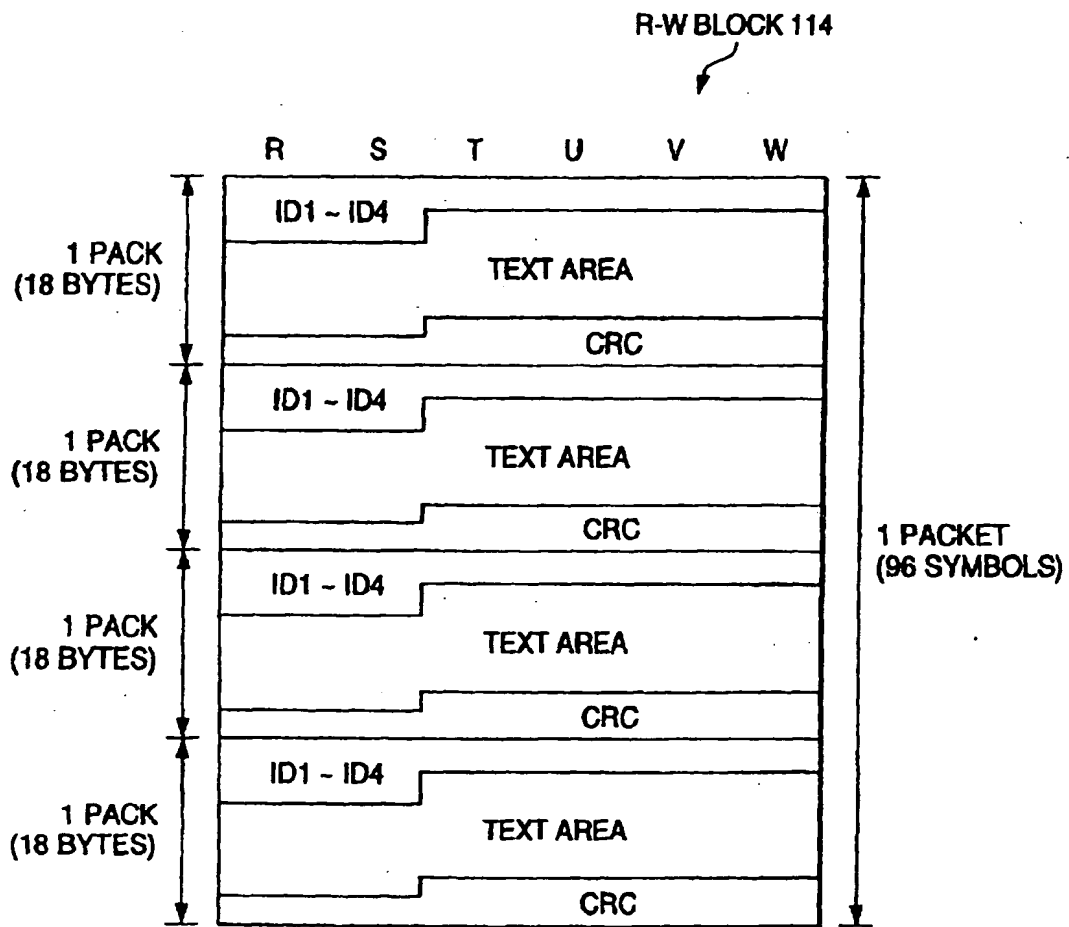
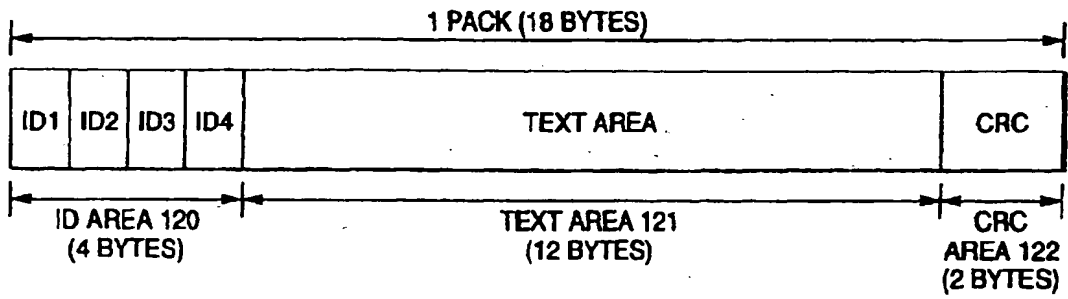


FIG. 7





**FIG. 8**



**FIG. 9**

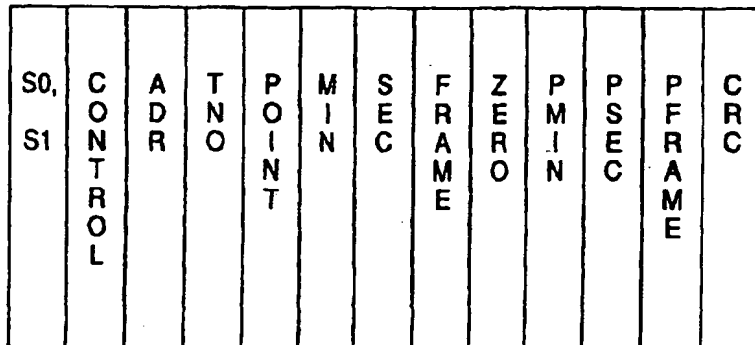


FIG. 10

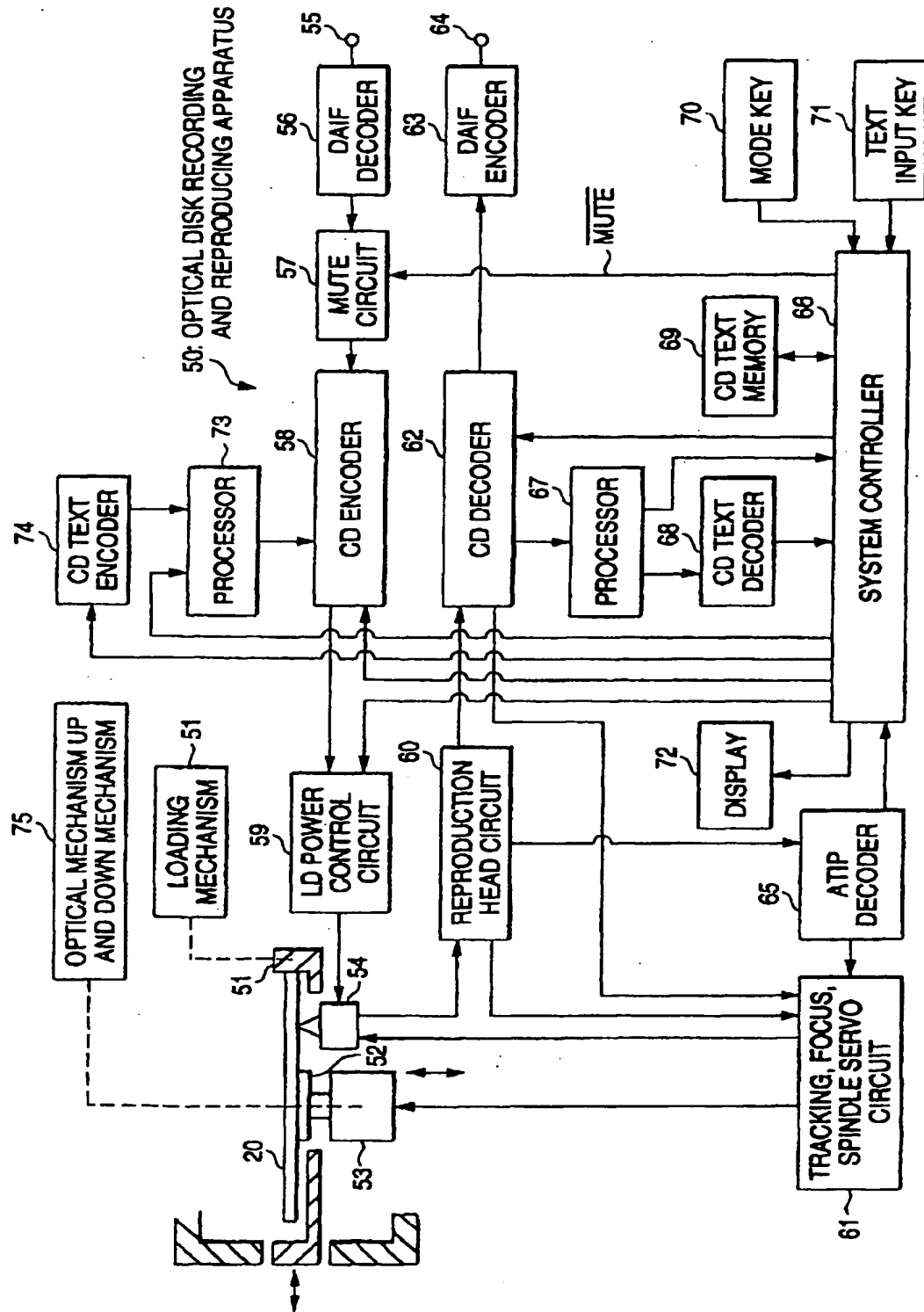


FIG. 11

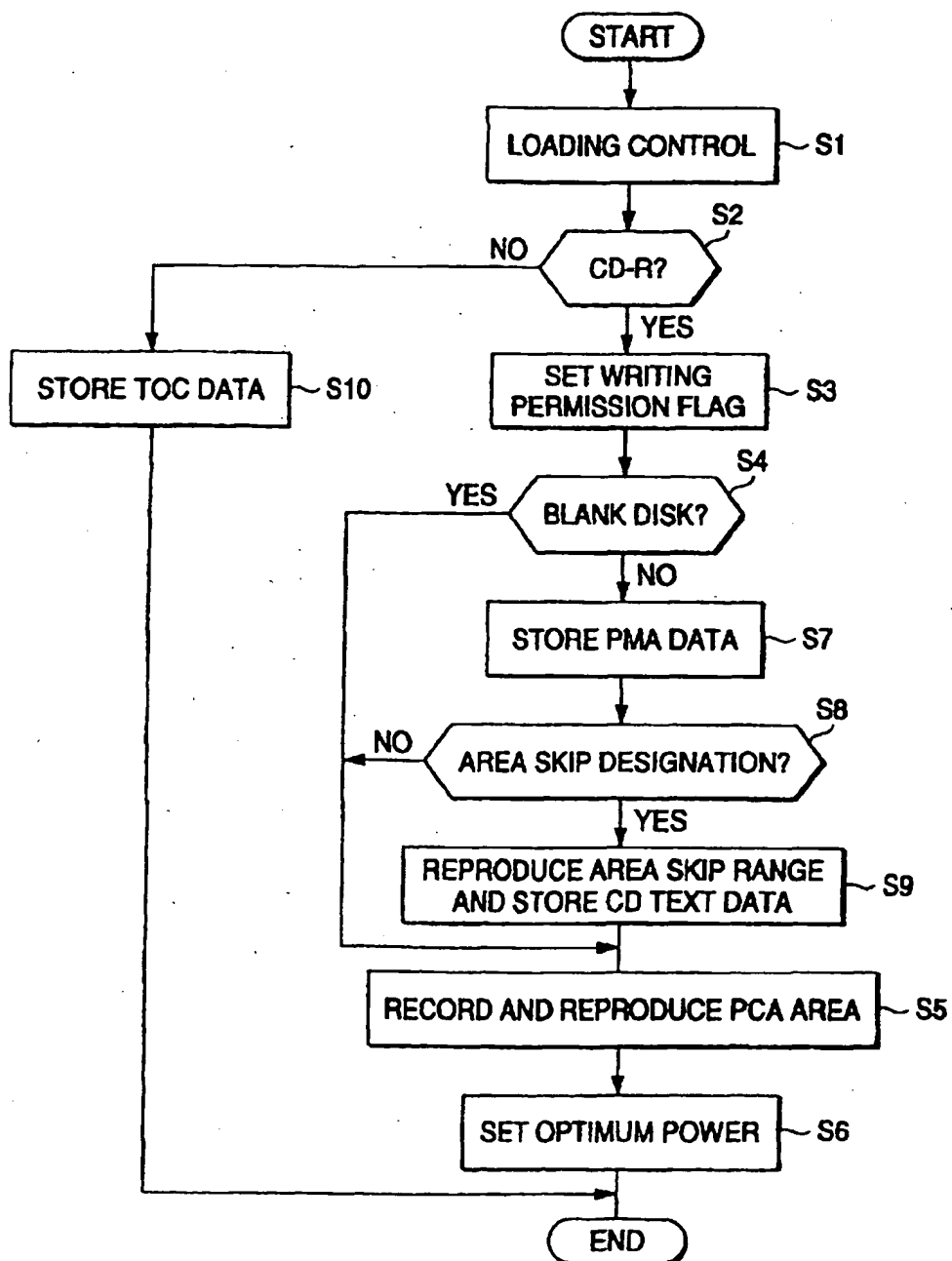


FIG. 12

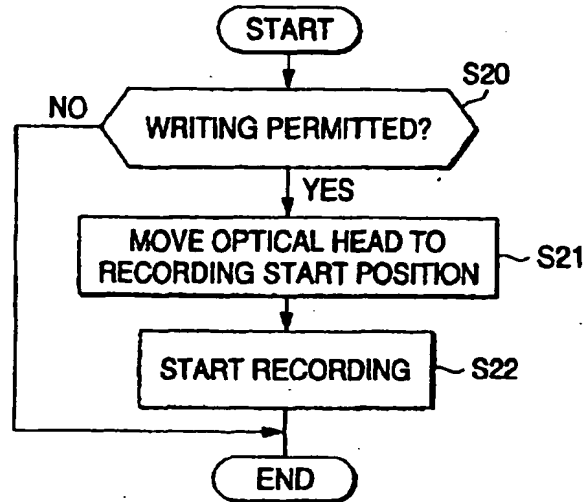


FIG. 13

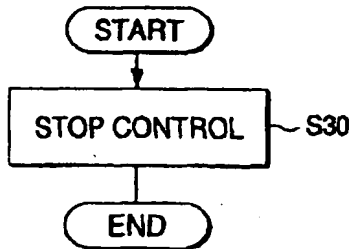


FIG. 14

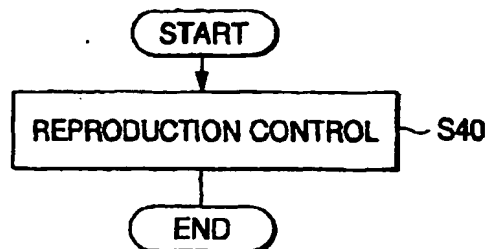


FIG. 15

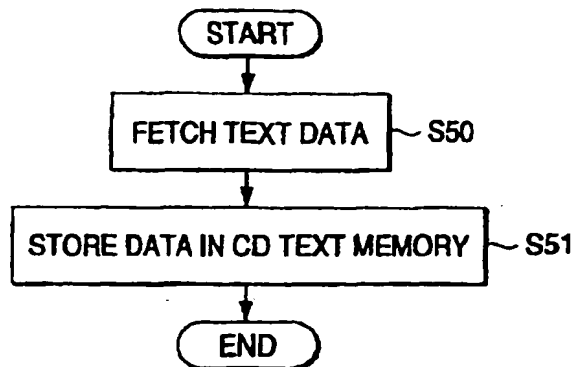


FIG. 16

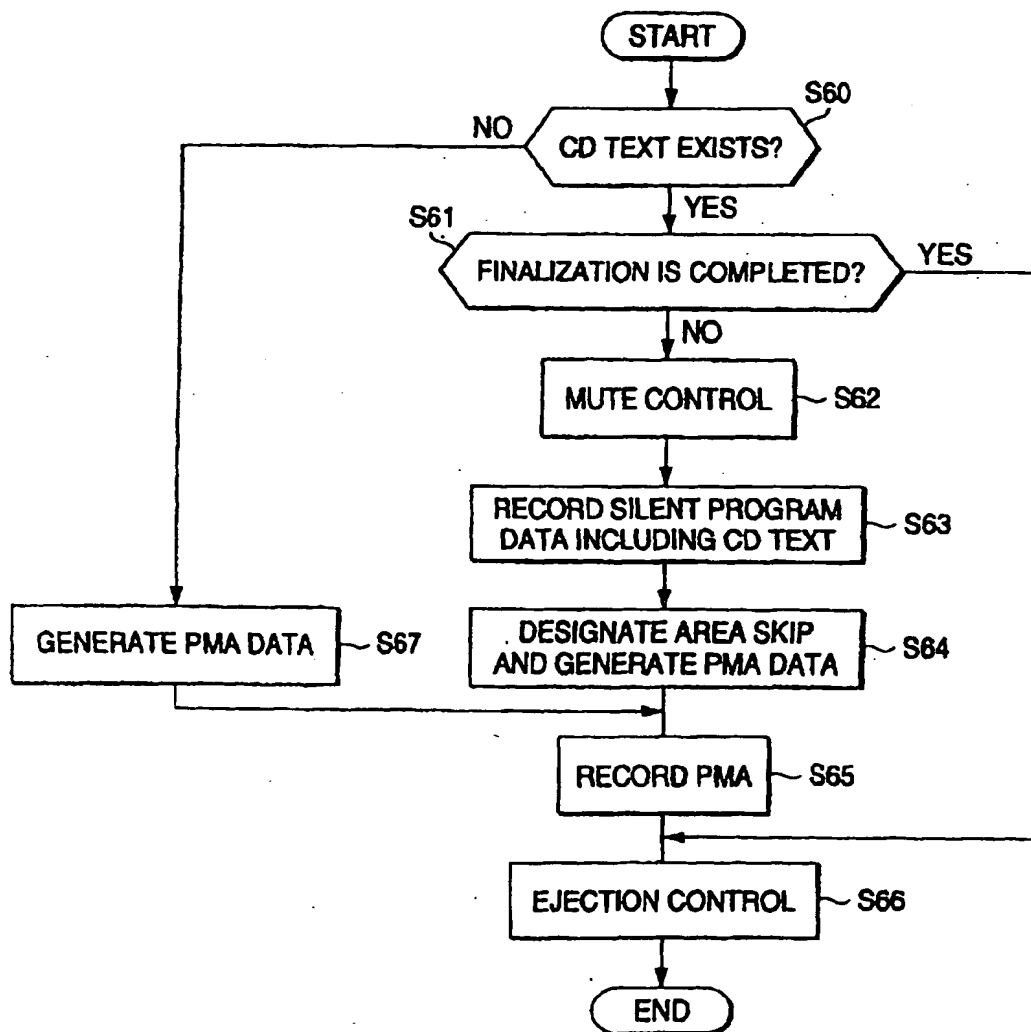
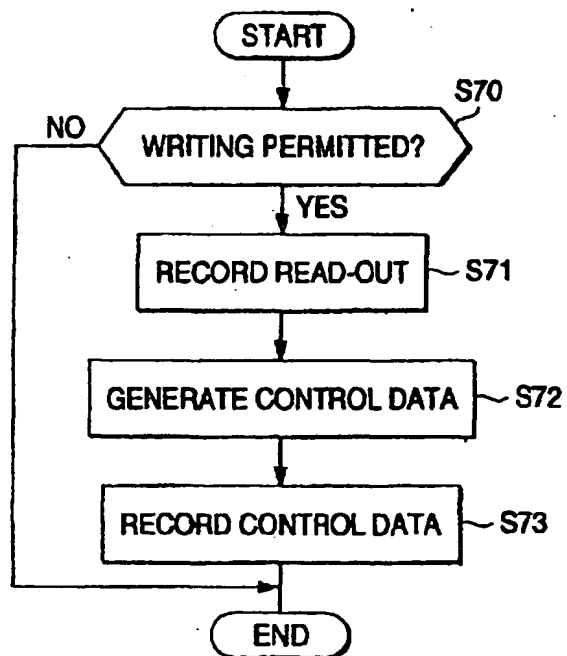


FIG. 17



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